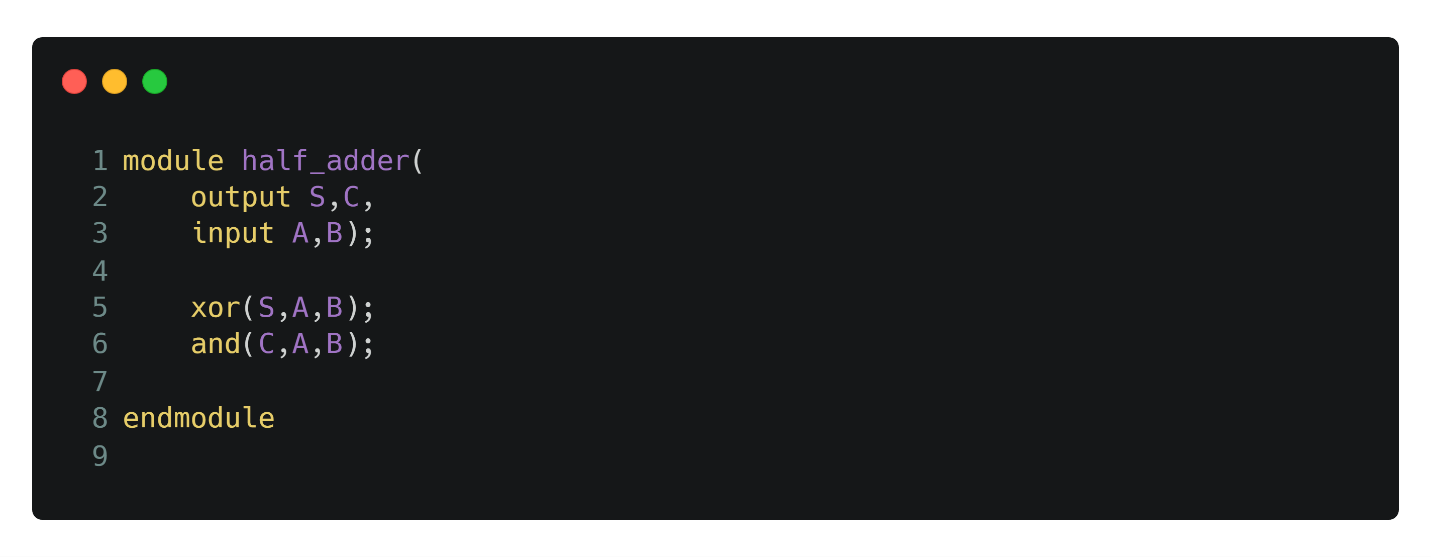
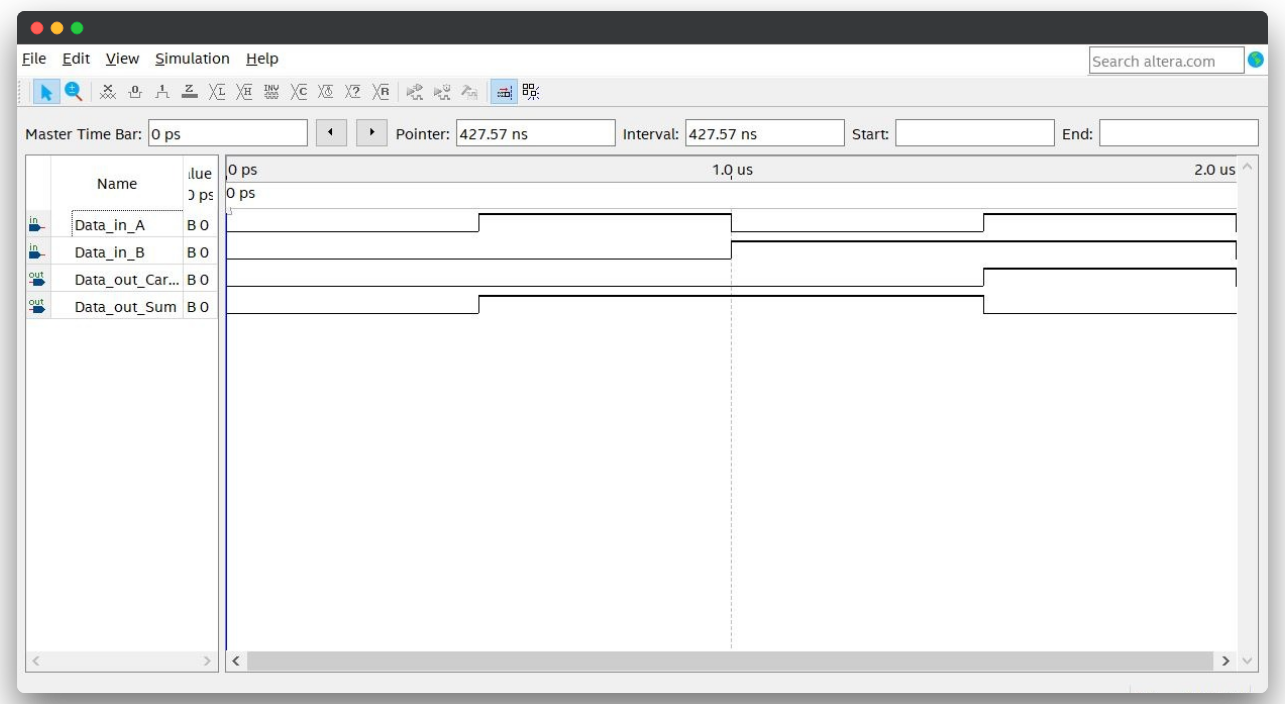
**Lab Assignment 4**

**Question 1**

Verilog Code

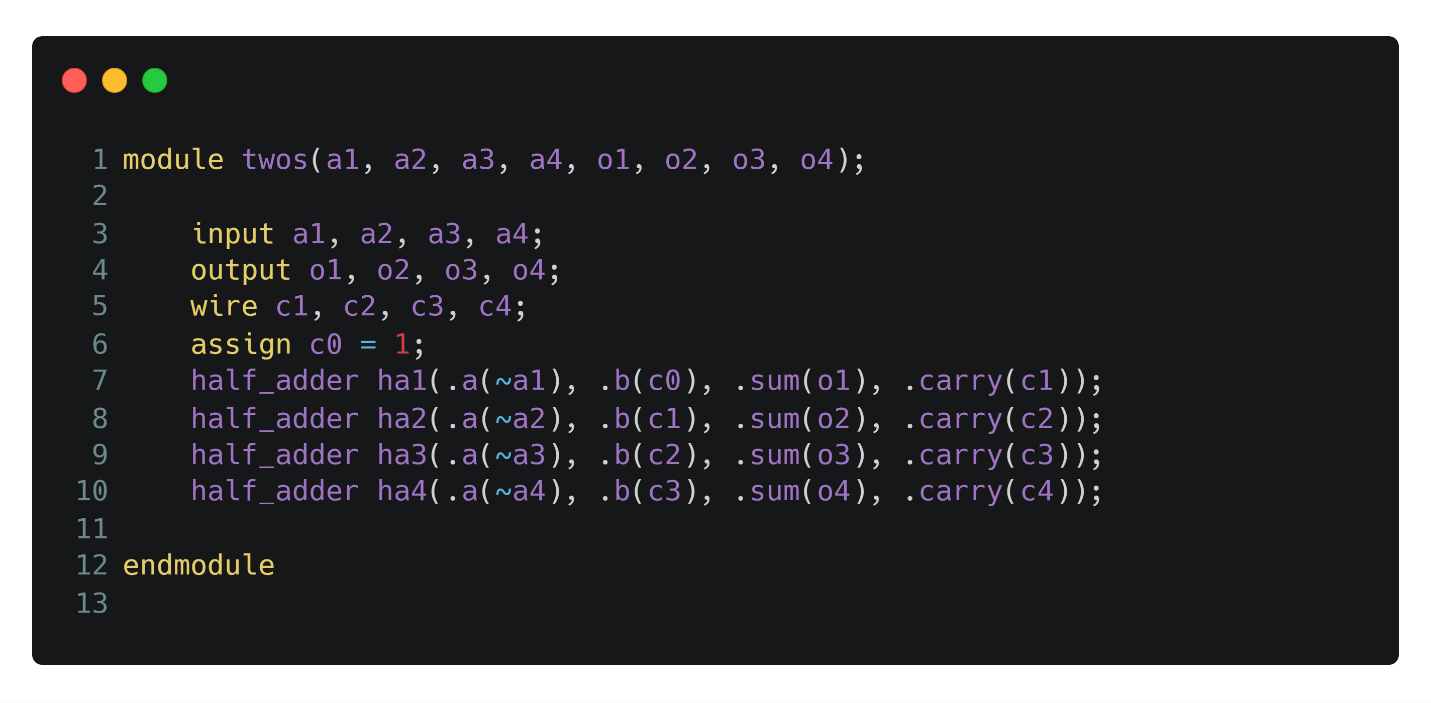


Quatrus Prime Waveform

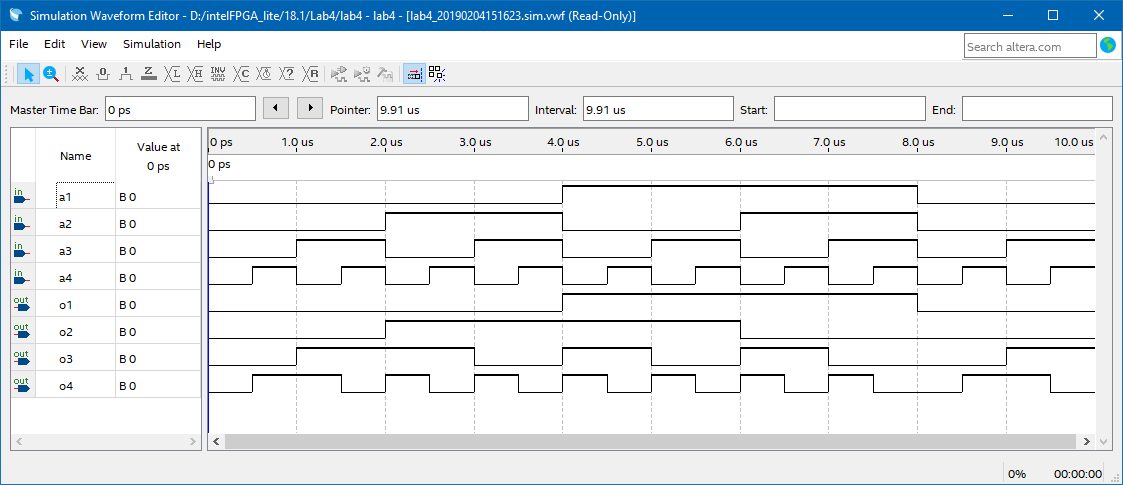


**Question 2**

Verilog Code

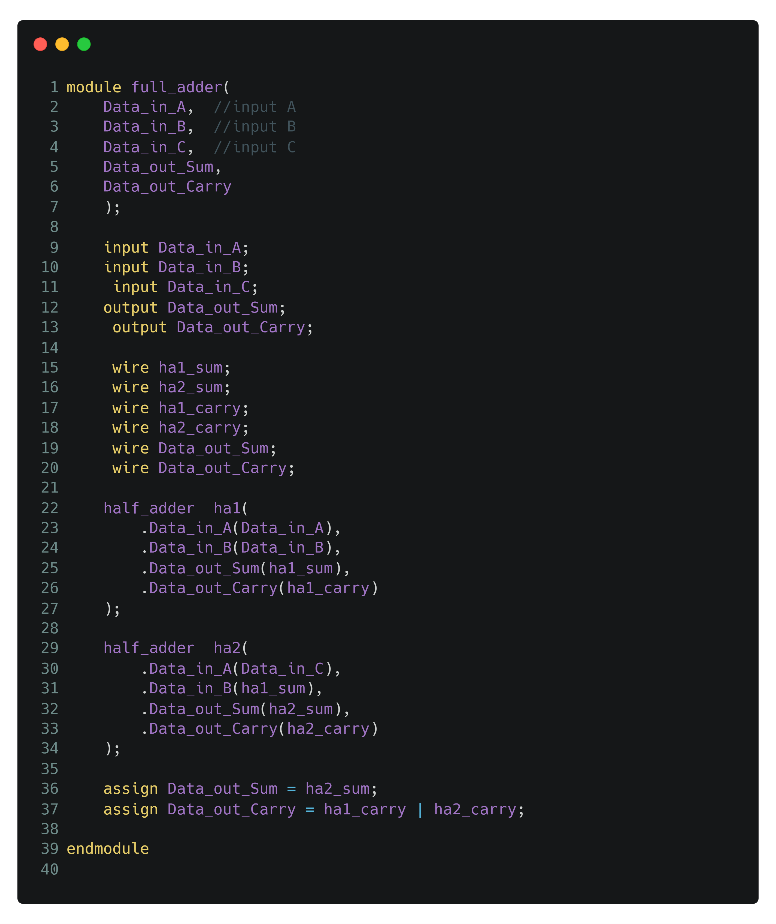


Quatrus Prime Waveform

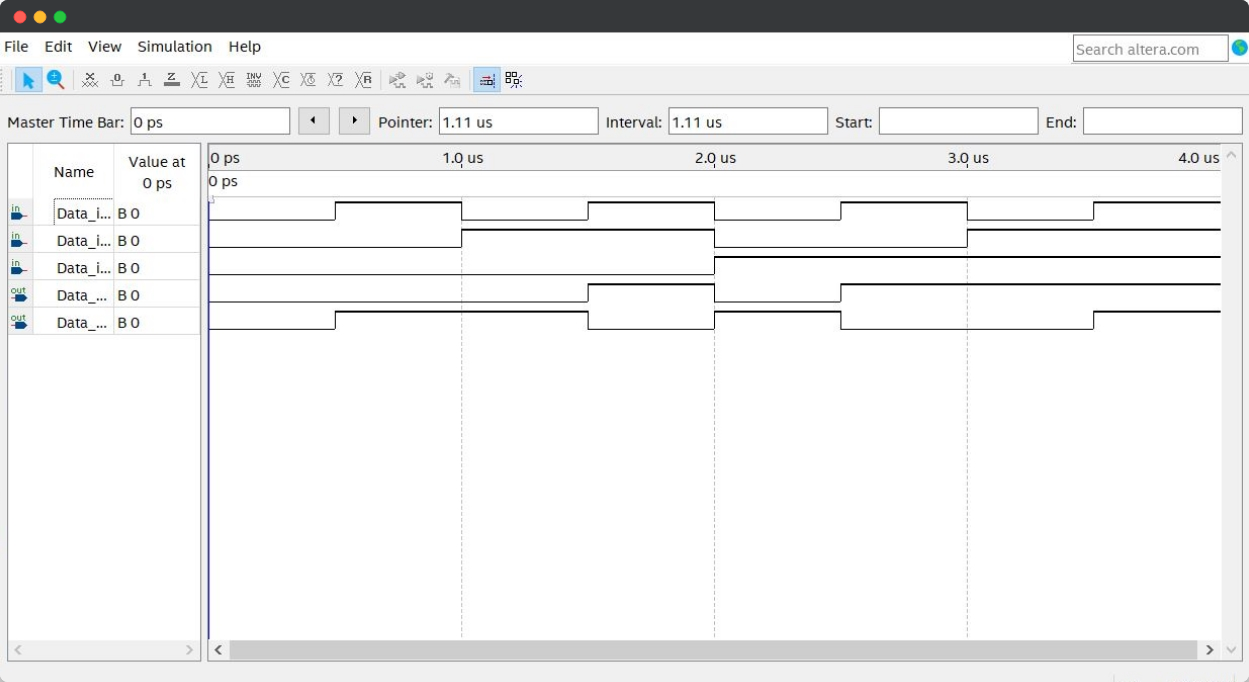


**Question 3**

Verilog Code

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Quatrus Prime Waveform

****

**Question 4**

Verilog Code



Quatrus Prime Waveform

